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1. Document ID: JP <u>07263669</u> A

L1: Entry 1 of 1

File: JPAB

Oct 13, 1995

DOCUMENT-IDENTIFIER: JP 07:43669 A
TITLE: SEMICONDUCTOR DEVICE AND ITS MANUFACTURE

PUBN-DATE: October 13, 1995

INVENTOR-INFORMATION:

NAME

COUNTRY

KUWABARA, MASASHI

ASSIGNEE-INFORMATION:

NAME

COUNTRY N/A

TOSHIBA CORP

APPL-NO: JP06056155 APPL-DATE: March 25, 1994

INT-CL (IPC): H01L 29/78

PURPOSE: To provide a semiconductor device having an anode-short structure wherein rukross. To provide a semiconductor device having an anode short street irregularity of the ON-voltage and the turn-off time is improved and irregularity of the ON-voltage and the turn-off time is improved and mass-productivity is excellent, and its manufacturing method.

CONSTITUTION: A P-type base region 2, an N-type source region 3, a gate electrode 7 and a source electrode 9 are formed on one main surface of an N-type semiconductor substrate 1 which turns to a drain region and a D-type anode region semiconductor substrate 1 which turns to a drain region and a D-type anode region semiconductor substrate 1 which turns to a drain region, and a P-type anode region 4 is formed on the other main surface of the semiconductor substrate 1. A trench is dug so as to perfectedly isolate the anode region 4, and an N-type short-circuit region 5 is formed so as to fill the trench. Thereby an anode-short structure is formed.

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	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	7301	(igot or (insulated adj gate adj bipolar adj transistor))		
2	BRS	L2	4265	1 and semiconductor	USPAT; US-PGI UB; EPO; JPO; DERWE T; IB	2001/04/22 12:53
3	BRS	L3	41	2 and (anode adj short)	USPAT US-PO UB; EPO; JPO; DERW T; I	2001/04/22 12:54

	Comments	ļ	Definition	Err
1				0
2				0
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